

WEST Search History

DATE: Friday, March 04, 2005

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L19	5940626.uref.	5
<input type="checkbox"/>	L18	5638525.pn.	1
<input type="checkbox"/>	L17	5940626.pn.	1
<input type="checkbox"/>	L16	((plural\$3 or multiple) with (isa\$1 or (instruction set architecture\$1))).ti.	5
<input type="checkbox"/>	L15	(L6 or programmable logic or pld) same L3 same L4	43
<input type="checkbox"/>	L14	5794062.uref.	29
<input type="checkbox"/>	L13	5794062.pn.	1
<input type="checkbox"/>	L12	423560.ap.	4
<input type="checkbox"/>	L11	6021186.uref.	3
<input type="checkbox"/>	L10	L8 and L9	1
<input type="checkbox"/>	L9	L7 same L6	3
<input type="checkbox"/>	L8	((plural\$3 or multiple) with (isa\$1 or (instruction set architecture\$1))).ti,ab,clm.	155
<input type="checkbox"/>	L7	((plural\$3 or multiple) with (isa\$1 or (instruction set architecture\$1)))	951
<input type="checkbox"/>	L6	(fpga or dpga or pal)	33375
<input type="checkbox"/>	L5	fpga and L3 and L4	9
	<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L4	microinstruction\$1 or micro-instruction\$1 or microcode or micro-code or micro-operation\$1 or microoperation\$1 or micro-op\$1 or microop\$1 or micro-program\$1 or microprogram\$1	14778
<input type="checkbox"/>	L3	macroinstruction\$1 or macro-instruction\$1 or macrocode or macro-code or macro-operation\$1 or macrooperation\$1 or macro-op\$1 or macroop\$1 or macro-program\$1 or macroprogram\$1 or cisc instruction	1443
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L2	(microcode or microinstruction\$1 or micro-code or micro-instruction\$1) and L1	10
<input type="checkbox"/>	L1	fpga and ((plural\$3 or multiple) with (isa\$1 or (instruction set architecture\$1)))	30

END OF SEARCH HISTORY